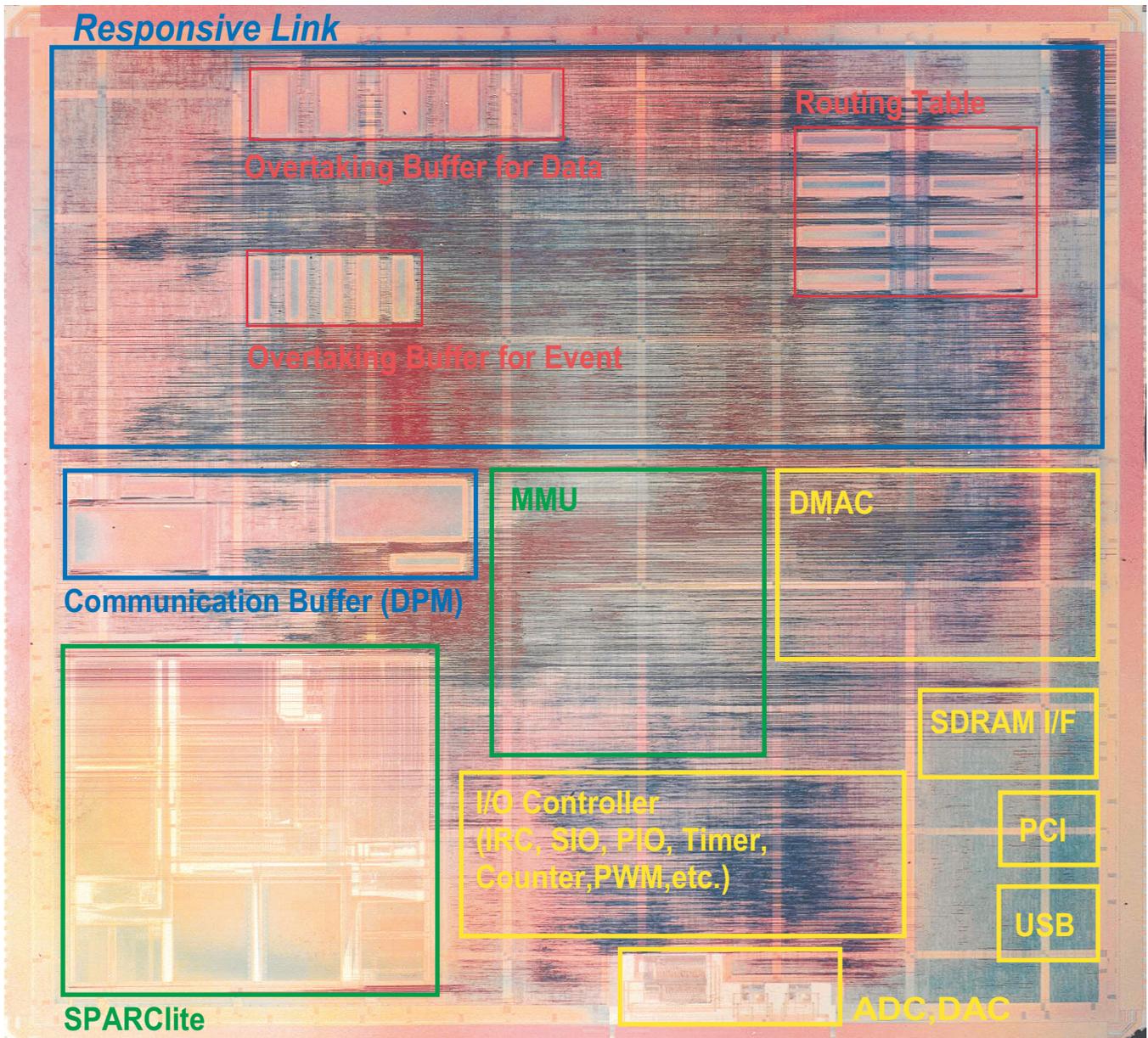


Responsive Processor for Parallel/Distributed Real-Time Control

Nobuyuki Yamasaki (Keio University / AIST)
E-mail: yamasaki@ics.keio.ac.jp, n.yamasaki@aist.go.jp
URL: <http://www.ny.ics.keio.ac.jp>



Responsive Processor integrates:

1. Processing Core
SPARC
2. Real-Time Communication
4 pairs of *Responsive Links*
3. Computer I/O Peripherals
SDRAM I/Fs, PCI, USB, DMAC, SIO, PIO, etc.
4. Control I/O Peripherals
ADCs, DACs, PWM Generators, Pulse Counters, etc.